

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
LEE.002

In Re Application Of: **Shone FUJA**

Serial No.
NEW

Filing Date
JANUARY 5, 2004

Examiner
TO BE ASSIGNED

Group Art Unit
TO BE ASSIGNED

Title: **NON-VOLATILE MEMORY ARRAY HAVING VERTICAL TRANSISTORS AND MANUFACTURING METHOD THEREOF**

Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

37 CFR 1.97(b)

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);

OR

☐ the fee set forth in 37 CFR 1.17(p).

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METHOD THEREOF**

Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
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Dated: **JANUARY 5, 2004**

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INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Docket Number (Optional) LEE.002		Application Number NEW
Applicant(s) Shone FUJA		
Filing Date January 5, 2004	Group Art Unit To Be Assigned	

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A	5,739,567	04/14/1998	WONG			
	B	5,770,514	06/23/1998	MATSUDA et al.			
	C	6,544,824	04/08/2003	PRADEEP et al.			
	D	6,365,452	04/02/2002	PERNG et al.			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	E	R. Muralidhar et al., "A 6V Embedded 90nm Silicon Nanocrystal Nonvolatile Memory", DigitalDNA Laboratories and the embedded Memory Center, 2003 IEEE, 4 pgs.
	F	B. De Salvo et al., "How far will Silicon nanocrystals push the scaling limits of NVMs technologies?", 2003 IEEE, 4 pgs.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.